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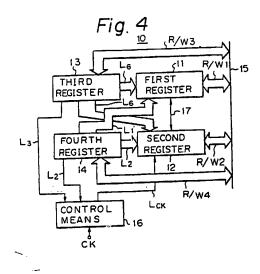
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(54) Reload-timer/counter circuit.

(12) A reload-timer/counter circuit provides a reloadtimer function and a counter function commonly and selectively. The circuit comprises first (11), second (12), third (13), and fourth (14) registers. The third (13) and fourth (14) registers act as a control status register and a mode register, respectively. The first (11) and second (12) registers act in the reload-timer mode as a data register and a counter register, respectively, whilst in the counter mode, the first (11) and second registers (12) act as the counter registers. The different operating modes are selected by control means (16).



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EUROPEAN SEARCH REPORT

Application Number

EP 89 30 6443

DOCUMENTS CONSIDERED TO BE RELEVANT					
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CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same catagory A: technological background O: non-written disclosure P: intermediate document T: theory or principle underlying the invention			E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document		